

UNITED STATES DEPARTMENT OF COMMERCE  
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Washington, D.C. 20231

SERIAL NUMBER	FILING DATE	FIRST NAMED INVENTOR	R	ATTORNEY'S DOCKET NO.
07/458,507	12/28/89	HORI		

BAKER, S EXAMINER

ANTONELLI, TERRY & WANDS  
1919 PENNSYLVANIA AVE., N.W., STE. 600  
WASHINGTON, DC 20006ART UNIT  
200PAPER NUMBER  
10DATE MAILED:  
04/19/80This is a communication from the examiner in charge of your application.  
COMMISSIONER OF PATENTS AND TRADEMARKS This application has been examined  Responsive to communication filed on 11/27/90  This action is made final.A shortened statutory period for response to this action is set to expire 3 month(s), — days from the date of this letter.  
Failure to respond within the period for response will cause the application to become abandoned. 35 U.S.C. 133

## Part I THE FOLLOWING ATTACHMENT(S) ARE PART OF THIS ACTION:

1. <input checked="" type="checkbox"/> Notice of References Cited by Examiner, PTO-892.	2. <input type="checkbox"/> Notice re Patent Drawing, PTO-948.
3. <input type="checkbox"/> Notice of Art Cited by Applicant, PTO-1449.	4. <input type="checkbox"/> Notice of Informal Patent Application, Form PTO-152
5. <input type="checkbox"/> Information on How to Effect Drawing Changes, PTO-1474.	6. <input type="checkbox"/>

## Part II SUMMARY OF ACTION

1.  Claims 7,9-13 and 15-19 are pending in the application.  
Of the above, claims \_\_\_\_\_ are withdrawn from consideration.
2.  Claims 8 and 14 have been cancelled.
3.  Claims \_\_\_\_\_ are allowed.
4.  Claims 7,9-13 and 15-19 are rejected.
5.  Claims \_\_\_\_\_ are objected to.
6.  Claims \_\_\_\_\_ are subject to restriction or election requirement.
7.  This application has been filed with informal drawings under 37 C.F.R. 1.85 which are acceptable for examination purposes.
8.  Formal drawings are required in response to this Office action.
9.  The corrected or substitute drawings have been received on \_\_\_\_\_. Under 37 C.F.R. 1.84 these drawings are  acceptable;  not acceptable (see explanation or Notice re Patent Drawing, PTO-948).
10.  The proposed additional or substitute sheet(s) of drawings, filed on \_\_\_\_\_, has (have) been  approved by the examiner;  disapproved by the examiner (see explanation).
11.  The proposed drawing correction, filed 11/27/90, has been  approved;  disapproved (see explanation).
12.  Acknowledgement is made of the claim for priority under U.S.C. 119. The certified copy has  been received  not been received  been filed in parent application, serial no. \_\_\_\_\_; filed on \_\_\_\_\_.
13.  Since this application appears to be in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.O. 11; 453 O.G. 213.
14.  Other

## EXAMINER'S ACTION

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1. Claims 7, 9-13 and 15-19 are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claims 7 and 13, a "control signal" apart from an "internal supply voltage" is not apparent, and it is not clear as to what is meant by "driving ability" in the context used.

2. The specification is objected to as failing to provide proper antecedent basis for the claimed subject matter. See 37 C.F.R. § 1.75(d)(1) and M.P.E.P. § 608.01(l). Correction of the following is required: A specific basis for a "control signal" and a controlled "driving ability" are not apparent.

3. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

4. Claims 7, 9-13 and 15-19 are rejected under 35 U.S.C. § 102(b) as being anticipated by Alaspa.

Alaspa discloses an on-chip power-on reset circuit providing a voltage limiter for MOS IC load circuits. Fig. 3 shows the claimed relationship between an external supply voltage  $V_R$  and an internal supply voltage  $V_{DD}$ , including a first rate A, a second rate E, a third rate C, and a fourth rate D.

5. Claims 7, 9-13 and 15-19 are rejected under 35 U.S.C. § 102(b) as being anticipated by Suzuki '243.

Suzuki '243 also discloses an on-chip power-on reset

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circuit, in bipolar technology. Fig. 2 corresponds to Fig. 3 in Alaspa.

6. The drawings are objected to because "oscillator" is misspelled in Fig. 32. Correction is required.

Applicant is required to submit a proposed drawing correction in response to this Office action. However, correction of the noted defect can be deferred until the application is allowed by the examiner.

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Dalrymple discloses a power-on reset circuit, with an internal supply voltage V6.

8. Applicant's arguments filed 11/27/90 have been fully considered but they are not deemed to be persuasive.

Applicant's arguments with respect to claims 7, 8-13 and 15-19 have been considered but are deemed to be moot in view of the new grounds of rejection.

The finality of the previous Office action (filed 2/25/91 as paper no. 8) is removed.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stephen Baker whose telephone number is (703) 308-3550.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 308-0754.

SMB  
April 17, 1991

  
STEPHEN M. BAKER